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Approved for use through 10/31/2002. OMB 0651-0031

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U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO				<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	10/056,055
				Filing Date	January 28, 2002
				First Named Inventor	Osamu NAKAMURA et al.
				Group Art Unit	2811
				Examiner Name	Unassigned
Sheet		of		Attorney Docket Number	0756-2428

**U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DHL		Kevin S. Jones et al., "Boron Diffusion Upon Annealing of Laser Thermal Processed Silicon," Ion Implantation Technology 2000, Conference on, 2000, pages 111-114	
DHL		D.J. Llewellyn et al., "Implantation and Annealing of Cu in InP for Electrical Isolation: Microstructural Characterisation," Optoelectronic and Microelectronic Materials and Devices Proceedings, 1996, pages 313-316	

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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> An applicant is to place a check mark here if English language Translation is attached.

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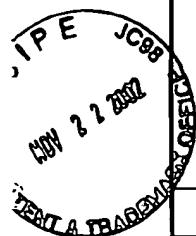
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Sheet 1 of 3

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Attorney Docket Number

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		Number	Kind Code <sup>3</sup> (if known)			
DHA	6,426,276	B1	Ohnuma et al.	07/30/2002		
	6,396,147	B1	Adachi et al.	05/28/2002		
	6,461,943	B1	Yamazaki et al.	10/08/2002		
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	5,589,694		Takayama et al.	12/31/1996		

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Sheet

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of

3

Attorney Docket Number

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		Number	Kind Code <sup>2</sup> (if known)			
OKL		5,595,923		Zhang et al.	01/21/1997	
		5,595,944		Zhang et al.	01/21/1997	
		5,604,360		Zhang et al.	02/18/1997	
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		5,614,426		Funada et al.	03/25/1997	
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		5,696,388		Funada et al.	12/09/1997	
		5,712,191		Nakajima et al.	01/27/1998	
		5,744,824		Kousai et al.	04/28/1998	
		5,843,225		Takayama et al.	12/01/1998	
		5,677,549		Takayama et al.	10/14/1997	

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		Office <sup>3</sup>	Number <sup>4</sup>				
OKL		JP	07-130652		05/19/1995		Abstract
OKL		JP	08-078329		03/22/1996		Abstract

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*Kenji Aki*

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Sheet 3 of 3

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Filing Date	January 28, 2002
First Named Inventor	Osamu NAKAMURA et al.
Group Art Unit	2811
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Attorney Docket Number	0756-2428

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
DHL		JP	09-074207			03/18/1997		Full
/		JP	2000-260777			09/22/2000		Abstract
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DHL		U.S. Patent Application Serial No. 10/097,641, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed March 15, 2002.	
/		U.S. Patent Application Serial No. 10/072,931, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed February 12, 2002.	
/		U.S. Patent Application Serial No. 10/074,050, including specification, drawings and filing receipt, "Method of Manufacturing a Semiconductor Device", Shunpei YAMAZAKI et al., filed February 14, 2002.	
/		U.S. Patent Application Serial No. 10/051,064, including specification, drawings and filing receipt, "Semiconductor Device and Method of Manufacturing the Same", filed January 18, 2002.	
/		U.S. Patent Application Serial No. 10/066,542, including specification, drawings and filing receipt, "Semiconductor Device and Method for Manufacturing the Same", filed February 5, 2002.	
/		U.S. Patent Application Serial No. 10/020,961, including specification, drawings and filing receipt, "Method of Manufacturing Semiconductor Device and Semiconductor Device", filed December 19, 2001.	
/		M. Miyake et al., "Characteristics of Buried-Channel pMOS Devices with Shallow Counter-Doped Layers Fabricated Using Channel Preamorphization", IEEE Transactions on Electron Devices, Vol. 43, No. 3, March 1996, pp. 444-449.	
/		L. S. Lee et al., "Argon Ion-Implantation on Polysilicon or Amorphous-Silicon for Boron Penetration Suppression in p+ pMOSFET", IEEE Transactions on Electron Devices, Vol. 45, No. 8, August 1998, pp. 1737-1744.	

Examiner Signature	<i>K. A. Johnson</i>	Date Considered	03-26-04
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<i>DM</i>		Specification and drawings of Application Serial No. 08/784,293 filed January 16, 1997 entitled "Fabrication Method of Semiconductor Device," along with pending claims as of June 7, 2002	

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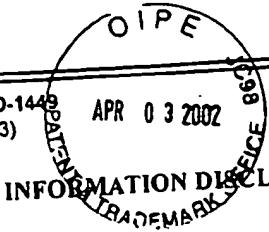
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## INFORMATION DISCLOSURE STATEMENT



Atty Docket 0756-2428

Serial No. 10/056,055

Applicants: Osamu NAKAMURA et al.

Filing Date: January 28, 2002

Group Art Unit: 2811

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DNN	5,529,937	06/25/1996	Zhang et al.			
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	5,814,540	09/29/1998	Takemura et al.			
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Kang, Donghee

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DM	6,316,789	11/13/2001	Yamazaki et al.					
DM	6,316,810	11/13/2001	Yamazaki et al.					
<b>FOREIGN PATENT DOCUMENTS</b>								
Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	No	
DM	05-109737	04/30/1993	JP			Full Eng		
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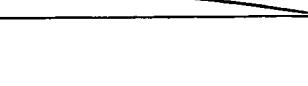
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				Application Number			
				Filing Date		January 28, 2002	
				First Named Inventor		Osamu NAKAMURA et al.	
				Group Art Unit			
				Examiner Name			
Sheet	1	of	1	Attorney Docket Number	740756-2428		
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number	Kind Code <sup>2</sup> (if known)				
DN		6,285,042		Ohtani et al.	09/04/2001		
		6,165,824		Takano et al.	12/26/2000		
		5,789,284		Yamazaki et al.	08/04/1998		
		5,923,962		Ohtani et al.	07/13/1999		
		5,643,826		Ohtani et al.	07/01/1997		
<b>FOREIGN PATENT DOCUMENTS</b>						T <sup>3</sup>	
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>3</sup>	Number <sup>4</sup>			Kind Code <sup>3</sup> (if known)	
DN		Japan	07-183540	Otani et al.	07/21/1995		Abstract
DN		Japan	3032801	Takano et al.	02/18/2000		Abstract
SL		Europe	0 651 431 A2	Ohtani et al.	01/31/1994		Full
<b>OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS</b>						T <sup>3</sup>	
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Examiner Signature		Date Considered	03-26-04				



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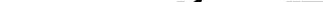
Substitute for form 1449 A/PTO		<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Application Number	10/056,055
		Filing Date	January 28, 2002
		First Named Inventor	Osamu NAKAMURA et al.
		Group Art Unit	2811
		Examiner Name	Unassigned
Sheet	1	of	1
		Attorney Docket Number	0756-2428

U.S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

#### **OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<i>JW</i>		Kevin S. Jones et al., "Boron Diffusion Upon Annealing of Laser Thermal Processed Silicon," Ion Implantation Technology 2000, Conference on, 2000, pages 111-114	
<i>DL</i>		D.J. Llewellyn et al., "Implantation and Annealing of Cu in InP for Electrical Isolation: Microstructural Characterisation," Optoelectronic and Microelectronic Materials and Devices Proceedings, 1996, pages 313-316	

Examiner Signature		Date Considered	03-26-04
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